

<b>Notice of References Cited</b>		Application/Control No. 09/955,902	Applicant(s)/Patent Under Reexamination STOJANCIC ET AL.	
		Examiner Longbit Chai	Art Unit 2131	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-2003/0031316	02-2003	Langston et al.	380/28
	B	US-2002/0039418	04-2002	Dror et al.	380/28
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Karl C. Posch "Modulo Reduction in Residue Number Systems" IEEE Transactions on Parallel and Distributed Systems, Vol. 6, No. 5, May 1995
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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